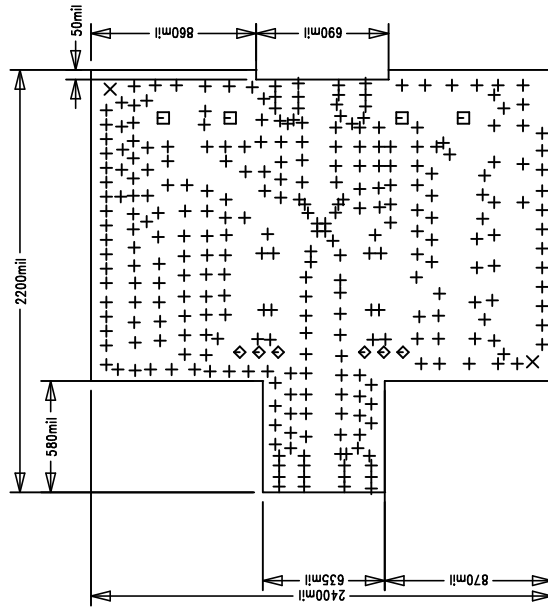


REVISIONS			
REV	DESCRIPTION	APPR	DATE
A	PROTOTYPE RELEASE		



Component Side

SIZE	QTY	SYM	PLTD	TOL
10	297	+	YES	+/-0.003
70	2	X	NO	+/-0.003
94	4	□	YES	+/-0.003
37	6	◇	YES	+/-0.003

NOTES : Unless Otherwise Specified

1. MATERIAL: EPOXY FIBERGLASS, NEMA GRADE FR-4. 1 OZ. COPPER ON INTERNAL LAYER. 2 OZ. COPPER ON OUTER LAYER.
THICKNESS .062 +/- .006 TOTAL OF 4 LAYERS
2. DRILLING: DRILL HOLES PER SCHEDULE. PLATE THROUGH HOLES WITH COPPER, .001 INCH THICK MIN. ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.
HOLE LOCATION TOLERANCES ARE +/- .003 INCH IN RELATION TO CENTER
3. SILKSCREEN : USING WHITE NON-CONDUCTIVE EPOXY INK
4. FINISH: SMOBC USING LPI BOTH SIDES COLOR GREEN.
WHITE TIN IMMERSION (OMIKRON) BOTH SIDES.
5. CONTROLLED 50 OHM IMPEDANCE (AT 6 GHz FREQ.) FOR LAYER 1-2 AND LAYER 4-3.
6. SUBJECT TO CHANGE BY MANUFACTURER, DEPENDING ON DIELECTRIC CONSTANT DEVIATIONS. PLEASE CONSULT LTC.
7. DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE.

FAB DRAWING

APPROVALS		1630 McCarthy Blvd. Milpitas, CA 95035 PH: (408)432-1900	
		LINEAR TECHNOLOGY	
DRAWN	INIT	DATE	
CHECK			
DESIGN	Antoninok		
ENGR	Doug S.		
TITLE: LT5528EUF		I/O MODULATOR FOR DAC INTERFACE	
SIZE	DEMO	DC984A	REV. 2
SCALE = NONE		SHT 1 of 1	